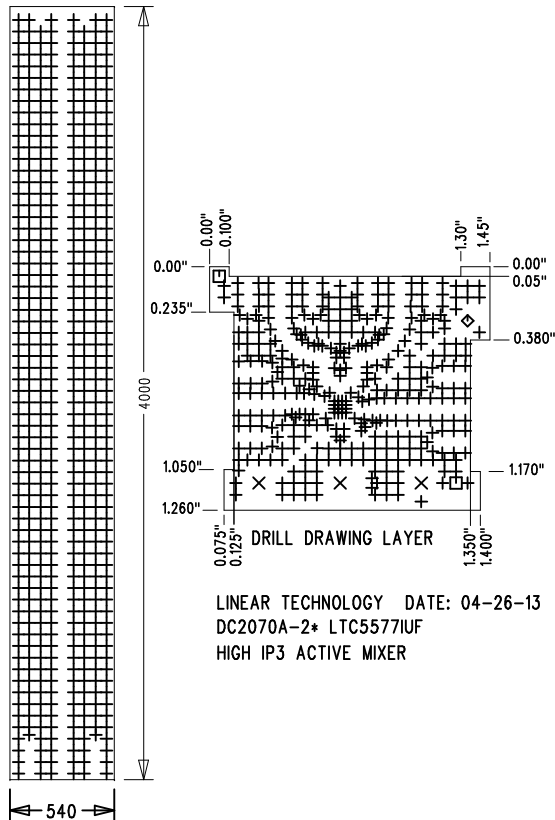


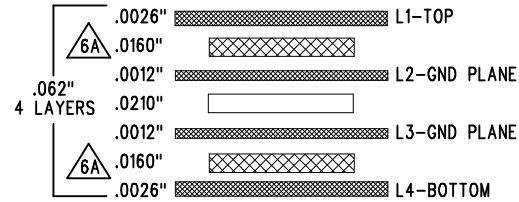
LTC DESIGNED
IMPEDANCE TEST
STRUCTURE COUPON



LINEAR TECHNOLOGY DATE: 04-26-13
DC2070A-2* LTC5577IUF
HIGH IP3 ACTIVE MIXER

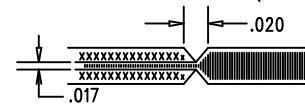
SIZE	QTY	SYM	PLATED	TOL
0.006	871	+	YES	+/-0.003
0.094	3	X	YES	+/-0.003
0.07	2	□	NO	+/-0.003
0.063	1	◇	YES	+/-0.003


LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NELCO 4000-13EP
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- CONTROLLED 50 OHM +/-5% IMPEDANCE FOR LAYER 1-2
TRACE WIDTH 30 MILS. MUST BE TESTED ON LTC DESIGNED IMPEDANCE TEST STRUCTURE COUPON.
- SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



UNLESS OTHERWISE SPECIFIED		APPROVALS		 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	AK		
		APP ENG.	WESTON S.	TITLE: FABRICATION DRAWING	
				HIGH IP3 ACTIVE MIXER	
				SIZE	IC NO.
				N/A	LTC5577IUF
					DEMO CIRCUIT 2070A
				SCALE = NONE	REV 2
				FILENAME: DC2070A-2.PCB	SHT 1 OF 1